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IN THE CLAIMS

Please amend claims 1, 3-6, 11, 16 and 20 as follows:

1. (Currently Amended) Apparatus for switching data from any of a plurality of inputs to any of a plurality of outputs, ~~said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "O" bit packs containing a number of bits "P", where O and P are integers, said apparatus comprising:~~

at least one apparatus for receiving ~~respective ones of said "O"~~ a plurality of input bit packs from each of said plurality of inputs organized in a combination of input data rails and time slots and for storing said received bit packs in matrix form including a storage position for each rail and time slot combination,

at least one apparatus for selecting any of the ~~respective~~ input bit packs from any of the rails in any of the time slots of said matrix, and

at least one apparatus for conveying said selected bit pack to any output data position within a combination of output data rails and time slots.

2. (Original) Apparatus of claim 1, wherein each bit pack is one bit wide.

3. (Currently Amended) Apparatus of claim 1, wherein ~~said apparatus for switching data is configured for selecting a plurality of input bit packs~~ are selected for output in a plurality of output data positions.

4. (Currently Amended) Apparatus of claim 1, wherein ~~said apparatus for switching data is configured for selecting a single bit pack~~ is selected for output in a plurality of output positions.

5. (Currently Amended) Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time

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~~slots on R2 rails, said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "O" bit packs containing a number of bits "P", where O and P are integers, said apparatus comprising:~~

~~at least one apparatus for receiving respective ones of said "O" input data arranged as bit packs from each of said N input positions in T time slots on R rails and for storing said received bit packs in matrix form including a storage position for each rail and time slot combination,~~

~~at least one apparatus for selecting any of the respective bit packs from any of the R rails and latching the selected bit packs during a predetermined time slot to thereby select a bit pack of predetermined R and T values, and~~

~~at least one apparatus for conveying said selected bit pack to any output position of predetermined R2 and T2 values.~~

6. Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, ~~said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "O" bit packs containing a number of bits "P", where O and P are integers, said apparatus comprising:~~

~~M selection blocks, each configured to select a bit pack of respective ones of said "O" input bit packs from each of said N input positions for a different one of the output positions, and each block including:~~

~~apparatus for receiving respective ones of said "O" input data arranged as bit packs in T time slots on R rails and for storing said received bit packs in matrix form including a storage position for each rail and time slot combination,~~

~~apparatus for selecting data from any of the R rails and latching the selected data during a predetermined time slot to thereby select a bit pack of predetermined R and T values, and~~

~~apparatus for conveying said selected bit pack to any output position of~~

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predetermined T2 and R2 values.

7. (Original) Apparatus of claim 6 further comprising:

a T2 X R2 output bit map configured for receiving a selected bit pack in each location from a different one of the M selection blocks.

8. (Original) Apparatus of claim 7 further comprising:

a second T2 X R2 output bit map configured to be loaded in parallel from the first output bit map.

9. (Original) Apparatus of claim 8 further comprising:

apparatus configured to arrange input bit packs as an array of T time slots on R rails and to convey output bit packs from the second T2 X R2 bit map on R2 rails in T2 time slots.

10. (Original) Apparatus of claim 9 wherein $N = M = 768$.

11. (Currently Amended) Apparatus for switching data from any of N input positions arranged as T time slots on R rails to any of M output positions arranged as T2 time slots on R2 rails, ~~said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "Q" bit packs containing a number of bits "P", where Q and P are integers, said apparatus comprising:~~

R2 selection blocks, each configured to select a bit pack ~~of respective ones of said "Q" input bit packs from each of said N input positions~~ for a different one of the output positions, and each block including:

apparatus for receiving ~~respective ones of said "Q" input~~ data arranges as bit packs on N rails,

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apparatus for selecting data from any of the N rails and for storing said received bit packs in matrix form including a storage position for each rail and time slot combination, and

apparatus for conveying said selected bit pack to any output position of predetermined T2 and R2 values.

12. (Original) Apparatus of claim 11 further comprising:

a T X R input bit map configured for receiving a selected bit pack in each location from a different one of the N space/time input positions.

13. (Original) Apparatus of claim 12 further comprising:

a second T X R input bit map configured to be loaded in parallel from the first input bit map and to convey N input bit packs to each of the R2 selection blocks and to hold the N input bit packs available to the R2 selection blocks during T2 time slots.

14. (Previously Presented) Apparatus of claim 11 further comprising:

apparatus configured to arrange input bit packs as an array of T time slots on R rails and to convey output bit packs from the second T2 X R2 bit map on R2 rails in T2 time slots.

15. (Original) Apparatus of claim 14 wherein $N = M = 768$.

16. (Currently Amended) A method of switching data from any of N input positions arranged as T time slots on R rails and stored in matrix form including a storage position for each time slot and rail combination to any of M output positions arranged as T2 time slots on R2 rails, ~~said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "Q" bit packs containing a number of bits "P", where Q and P are integers, said method~~ comprising the steps of:

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- (a) in each of R2 selection blocks, selecting a bit pack from any of the storage positions of said matrix ~~of respective ones of said "O" input bit packs from each of said N input positions~~ for a different one of the output positions, and
(b) conveying each of the bit packs selected in step (a) to the associated one of the output positions.

17. (Original) The method of claim 16 wherein step (a) comprises the further step of:
(c) receiving input data arranged as bit packs on N rails.

18. (Previously Presented) The method of claim 17 wherein step (a) comprises the further step of:
(d) selecting a bit pack from any of the N rails.

19. (Original) The method of claim 18 wherein step a comprises the further step of:
(e) conveying said selected bit pack to an output position of predetermined T2 and R2 values.

20. (Currently Amended) A method of switching data from any of N input positions arranged as T time slots on R rails and stored in matrix form including a storage position for each time slot and rail combination to any of M output positions arranged as T2 time slots on R2 rails, ~~said data formatted as data blocks containing a fixed number of bits of data, each data block comprising "O" bit packs containing a number of bits "P", where O and P are integers, said method comprising the steps of:~~

- (a) in each of M selection blocks, selecting a bit pack from any of the storage positions of said matrix ~~of respective ones of said "O" input bit packs from each of said N input positions~~ for a different one of the output positions, and
- (b) conveying each of the bit packs selected in step (a) to the associated one of the output positions.

21. (Previously Presented) The method of claim 20 wherein step (a) further comprises the steps of:

- (c) receiving input data arranged as bit packs in T time slots on R rails, and
- (d) selecting data from any of the R rails and latching the selected data during a predetermined time slot to thereby select a bit pack of predetermined R and T values.

22. (Previously Presented) The method of claim 21 wherein step (b) further comprises the step of:

- (e) conveying said selected bit pack to any output position of predetermined T2 and R2 values.

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